Menoufiya University
Faculty of Engineering
Shebin El-Kom
Final Exam
Academic Year: 2017-2018

... Department: Electrical Engineering.
Year: Master.
Subject/Code: Design of power electronics circuits Time Allowed: 3 hours
Date: $13 / 1 / 2018$

Remarks: No. of pages: $2 \quad$ No. of questions: 6 Allowed Tables and Charts: (None) Assume any required data

## Answer the following Questions [100Mark] (جب عن الأسئلة التالية ( 100 درجة

| Ouestion (1) (20Marks) |  | Marks |  |
| :---: | :---: | :---: | :---: |
| [a] | Compare between power electronics controlled switc IGBT from point of view basic of operation, Ratings best applications. | hyristor, GTO, BJT, MOSFET and hing frequency, commutation and | [7] |
| [b] | The thyristor in fig. (1), is used to control power delivered to the load, supply voltage is DC source with 400 V , maximum allowable di/ dt and dv/dt for thristor are $50 \mathrm{a} / \mu \mathrm{sec}$ and $200 \mathrm{v} / \mu \mathrm{sec}$ respectively. Determine the values of the inductor " L " and snubber circuit components Rs and Cs. | Fig. 1 | ${ }^{[13}$ |

Ouestion (2)
(25Marks)
Marks

| [a] | Discus the fault finding procedure in power ele section of the circuit? | circuits and fault clearance steps for | [5] |
| :---: | :---: | :---: | :---: |
| [b] | A full bridge single phase inverter shown in fig. 2 used to fed a load with 1 Kw , supplied from solar cell system. the load parameters are: <br> 220 volt, 50 Hz , at duty cycle with turn on time 0.01 sec . for all switches, power factor 0.85 and ripple voltage $5 \%$ of output and ripple current is $20 \%$ of load current. Assume the transformer loaded by $80 \%$ of full load power. Design the inverter to choose all switches data and inductance, capacitor at output terminals. | Fig. 2 | [20] |

## Ouestion (3)

(25Marks)
[a] An AC Fly back Converter shown in the figure(3) with input power 100 W , input voltage 35 v ,output voltage 35 v , line frequency 60 hz at maximum duty cycle 0.5 and the switching frequency is 12 khz . The converter is an ideal one. design the fly back converter to choose the inductance Lm, output capacitor, the input power factor at full load and the parameter of the input filter and Calculate the output voltage transferred to transformer primary side and the turns. ratio. Assume that the input filter capacitor is $20 \mu \mathrm{~F}$, the maximum current at primary side 10.5 A and the output voltage ripple 2 v .


Ouestion (4)


| Field | National Academic Reference Standard(NARS) |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Knowledge \& Understanding |  |  |  | Intellectual Skills | Professional Skills |  |  | General Skills |
| Course ILOs | a-4-1 | a-8-1 | a-8-2 | a-19-1 | b-2-1 | c-13-1 | c-13-2 | c-17-1 | ----- |
| Question No. | $\begin{aligned} & \text { 1(a), } \\ & \text { 3(a) } \end{aligned}$ | $\begin{aligned} & \text { 1(b), } \\ & 3(\mathrm{a}), \end{aligned}$ | $\begin{aligned} & 1(\mathrm{~b}), \\ & 2(\mathrm{a}, \mathrm{~b}), \\ & 4(\mathrm{a}, \mathrm{~b}), \end{aligned}$ | $\begin{aligned} & 2(\mathrm{a}), \\ & 3(\mathrm{a}), \end{aligned}$ | 3(a), | $\begin{aligned} & \text { 1(b), } \\ & \text { 2(a), } \end{aligned}$ | 3(a), | $\begin{aligned} & \text { 2(a), 3(a), } \\ & \text { 4(b), } \end{aligned}$ | ------ |

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