[25]

Menoufiya University Faculty of Engineering Shebin El-Kom Final Exam Academic Year: 2017-2018



 Department: Electrical Engineering. Year: Master.
 Subject/Code: Design of power electronics circuits Time Allowed: 3 hours
 Date: 13 / 1/2018

Remarks:No. of pages: 2No. of questions: 6Allowed Tables and Charts: (None)Assume any required data

أجب عن الأسئلة التالية (100 درجة) [100Mark] (جب عن الأسئلة التالية (

 [a] Compare between power electronics controlled switches Thyristor, GTO, BJT, MOSFET and IGBT from point of view basic of operation, Ratings, switching frequency, commutation and best applications. [b] The thyristor in fig. (1), is used to control power delivered to the load, supply voltage is DC source with 400 V, maximum allowable di/ dt and dv/dt for thristor are 50 a/µsec and 200 v/ µ sec respectively. Determine the values of the inductor "L" 	0	uestion (1) (20Marks)	Marl	KS
 [a] IGBT from point of view basic of operation, Ratings, switching frequency, commutation and best applications. [b] The thyristor in fig. (1), is used to control power delivered to the load, supply voltage is DC source with 400 V, maximum allowable di/ dt and dv/dt for thristor are 50 a/µsec and 200 v/ µ sec respectively. Determine the values of the inductor "L" 		Compare between power electronics controlled switches	Thyristor, GTO, BJT, MOSFET and	[7]
best applications. [b] The thyristor in fig. (1), is used to control power delivered to the load, supply voltage is DC source with 400 V, maximum allowable di/ dt and dv/dt for thristor are 50 a/μsec and 200 v/ μ sec respectively. Determine the values of the inductor "L"	[a]	IGBT from point of view basic of operation, Ratings, sw	tching frequency, commutation and	
[b] The thyristor in fig. (1), is used to control power delivered to the load, supply voltage is DC source with 400 V, maximum allowable di/ dt and dv/dt for thristor are 50 a/µsec and 200 v/ µ sec respectively. Determine the values of the inductor " L" [13]		best applications.		
delivered to the load, supply voltage is DC source with 400 V, maximum allowable di/ dt and dv/dt for thristor are 50 a/µsec and 200 v/ µ sec respectively. Determine the values of the inductor "L"	[b]	The thyristor in fig. (1), is used to control power		[13
with 400 V, maximum allowable di/ dt and dv/dt sw for thristor are 50 a/µsec and 200 v/ µ sec sw respectively. Determine the values of the inductor " L"		delivered to the load, supply voltage is DC source]
for thristor are 50 a/ μ sec and 200 v/ μ secVin = 400 Vrespectively. Determine the values of the inductor "L"Vin = 400 V		with 400 V, maximum allowable di/ dt and dv/dt	sw 7	
respectively. Determine the values of the inductor "L"	•	for thristor are 50 a/μsec and 200 v/ μ sec	Vin = 400 V	
		respectively. Determine the values of the inductor " L"	·	
and snubber circuit components Rs and Cs. Fig.1		and snubber circuit components Rs and Cs.	Fig.1	



Question (3)(25Marks)[a]An AC Fly back Converter shown in the figure(3) with input
power 100 W, input voltage 35v, output voltage 35v, line
frequency 60 hz at maximum duty cycle 0.5 and the switching
frequency is 12 khz. The converter is an ideal one.
design the fly back converter to choose the inductance Lm,
output capacitor, the input power factor at full load and the
parameter of the input filter and Calculate the output
voltage transferred to transformer primary side and the turns
ratio. Assume that the input filter capacitor is 20 μ F, the
maximum current at primary side 10.5A and the output
voltage ripple 2v.Fig. 3

Qu	estion (4) (30Marks)	Mark	S
[a]	Discuss the principle of operation of cyclo-converter ?		[10]
[b]	The DC-DC switching boost converter will take a 5 Volt DC voltage supply and deliver 15 Volts to the load . The maximum current delivered to the load will be 0.4 A . The switching frequency of the MOSFET equal 50 KHZ and inductor ripple current will be 10% of the output current . The output voltage ripple will be 5% of the output voltage . Design the power circuit of this Boost Converter .	$\begin{array}{c c} & & L & D & lour \\ \hline & & & & & \\ \hline V_{IN} & & & \\ \hline & & & \\ \hline & & & \\ \hline \\ \hline$	[20]
		Fig. 4-a	

	National Academic Reference Standard(NARS)								
Field	Knowledge & Understanding			Intellectual Skills Professional Skills			General Skills		
Course ILOs	a-4-1	a-8-1	a-8-2	a-19-1	b-2-1	c-13-1	c-13-2	c-17-1	
Question No.	1(a), 3(a)	1(b), 3(a),	1(b), 2(a,b), 4(a,b),	2(a), 3(a),	3(a),	1(b), 2(a),	3(a),	2(a), 3(a), 4(b),	

انتهت الأسئلة مع أطيب الأمنيات بالتوفيق أ.د / عوض السيد السبع

.

1